	Sel(3)	T(2)	Sel P1	P1 L	P1 O	SP L	SP O	X L	X O	Y L	Y O	RT L	RT O	R1 L	R1 O	R2 L	R2 O	R3 L	R3 O	R4 L	R4 O	IR load	IR out	MAR load	MAR out	MDR load	MDR out	MDR mem	RW	PC load	PC out	PC init	Sel ACC	ACC load	ALU code	Fi	Programa de control	Instrucció
																Bits d'adreça de salt																								
0	000	00	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	If not inici GOTO 0	Inici
1	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	00000	0	EXE (Init PC)	
2	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	00000	0	EXE (MAR ← PC)	Fetch
3	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
4	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	1	0	0	0	00100	0	EXE (MDR MEM(MAR)) EXE PC ++	
5	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (IR ← MDR)	
6	100	00	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	00000	0	IF NOT INTERRUP GOTO 9	
7	000	10	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	10000	0	EXE (SP ← PC - 1)	
8	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 162	
9	000	11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	BRA	
10	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	NOOP
11	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	LDR1
12	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
13	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	
14	000	10	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (R1 ← MDR)	
15	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
16	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	LDR2
17	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
18	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	
19	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (R2 ← MDR)	
20	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
21	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	ADD
22	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00010	0	EXE (RT ← R2 + ACC)	
23	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
24	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	STR1
25	000	10	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	00000	0	EXE (MDR ← R1)	
26	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	00000	0	EXE (escriptura en RAM)	
27	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
28	000	10	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	CMP
29	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00001	0	EXE (R2 - ACC)	
30	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
31	001	00	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	IF NOT C GOTO 2	BRE
32	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	00011	0	EXE (PC ← IR) amb mask de 8 bits	
33	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
34	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	00011	0	EXE (PC ← IR) amb mask de 8 bits	JMP

	Sel(3)	T(2)	Sel P1	P1 L	P1 O	SP O	SP L	X L	X O	Y L	Y O	RT L	RT O	R1 L	R1 O	R2 L	R2 O	R3 L	R3 O	R4 L	R4 O	IR load	IR out	MAR load	MAR out	MDR load	MDR out	MDR mem	RW	PC load	PC out	PC init	Sel ACC	ACC load	ALU code	F i	Programa de control	Instrucció
																Bits d'adreça de salt																								
35	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	JMP
36	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	SUBS
37	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00001	0	EXE (RT ← ACC – R2)	
38	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
39	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	STR2
40	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	00000	0	EXE (MDR ← R2)	
41	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	00000	0	EXE (escriptura en RAM)	
42	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
43	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	1	Fi	FI
44	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 0	
45	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	SRLR1
46	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (R2 ← IR) amb mask de 8 bits	
47	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00101	0	EXE (R1 ← R2 >> ACC)	
48	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
49	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R2)	SLLR2
50	000	10	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (R1 ← IR) amb mask de 8 bits	
51	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00110	0	EXE (R2 ← R1 << ACC)	
52	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
53	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	LDRT
54	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
55	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	
56	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (RT ← MDR)	
57	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
58	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	SWAP R1,R2
59	000	10	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00010	0	EXE (R1 ← R2 + ACC)	
60	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	
61	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00001	0	EXE (R2 ← ACC – R2)	
62	000	10	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00001	0	EXE (R1 ← ACC – R2)	
63	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
64	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	AND
65	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00111	0	EXE (RT ← R2 AND ACC)	
66	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
67	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	OR

	Sel(3)	T(2)	Sel P1	P1 L	P1 O	SP L	SP O	X L	X O	Y L	Y O	RT L	RT O	R1 L	R1 O	R2 L	R2 O	R3 L	R3 O	R4 L	R4 O	IR load	IR out	MAR load	MAR out	MDR load	MDR out	MDR mem	RW	PC load	PC out	PC init	Sel ACC	ACC load	ALU code	F i	Programa de control	Instrucció
																Bits d'adreça de salt																								
68	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01000	0	EXE (RT ← R2 OR ACC)	OR
69	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
70	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	XOR
71	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01001	0	EXE (RT ← R2 XOR ACC)	
72	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
73	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	NAND
74	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01010	0	EXE (RT ← R2 NAND ACC)	
75	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
76	000	10	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (RT ← R1)	NOT R1
77	000	10	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01100	0	EXE (R1 ← NOT RT)	
78	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
79	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (RT ← R2)	NOT R2
80	000	10	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01100	0	EXE (R2 ← NOT RT)	
81	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
82	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	MUL
83	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01101	0	EXE (RT ← ACC * R2)	
84	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
85	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	DIV
86	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01110	0	EXE (ACC / R2)	
87	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01110	0	EXE (ACC / R2)	
88	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01110	0	EXE (RT ←ACC / R2)	
89	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
90	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	MOD
91	000	10	0	00	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01111	0	EXE (ACC % R2)	
92	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01111	0	EXE (ACC % R2)	
93	000	10	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	01111	0	EXE (RT ←ACC % R2)	
94	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
95	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	DEC R1
96	000	10	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10000	0	EXE (R1 ← R1 – 1)	
97	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
98	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R2)	DEC R2
99	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10000	0	EXE (R2 ← R2 – 1)	
100	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
101	000	10	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R1)	INC R1

	Sel(3)	T(2)	Sel P1	P1 L	P1 O	SP L	SP O	X L	X O	Y L	Y O	RT L	RT O	R1 L	R1 O	R2 L	R2 O	R3 L	R3 O	R4 L	R4 O	IR load	IR out	MAR load	MAR out	MDR load	MDR out	MDR mem	RW	PC load	PC out	PC init	Sel ACC	ACC load	ALU code	F i	Programa de control	Instrucció
																Bits d'adreça de salt																								
102	000	10	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00100	0	EXE (R1 ← R1 + 1)	INC R1
103	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
104	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	00000	0	EXE (ACC ← R2)	INC R2
105	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00100	0	EXE (R2 ← R2 + 1)	
106	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
107	000	10	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (R1 ← RT)	MOV R1, RTEMP
108	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
109	000	10	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (R2 ← RT)	MOV R2, RTEMP
110	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
111	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	STRT
112	000	10	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	00000	0	EXE (MDR ← RT)	
113	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	00000	0	EXE (escriptura en RAM)	
114	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
115	000	10	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	10000	0	EXE (X ← X – 1)	DJNZX
116	011	00	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	IF ZERO GOTO 2	
117	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	00011	0	EXE (PC ← IR) amb mask de 8 bits	
118	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
119	010	00	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	IF NOT NEGATIVE GOTO 2	BRNEG
120	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	00011	0	EXE (PC ← IR) amb mask de 8 bits	
121	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
122	011	00	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	IF ZERO GOTO 2	BRNE
123	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	00011	0	EXE (PC ← IR) amb mask de 8 bits	
124	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
125	000	10	0	00	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	10001	0	EXE (R1 ← IR) amb mask de 16 bits	R1 = num
126	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
127	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	10001	0	EXE (R2 ← IR) amb mask de 16 bits	R2 = num
128	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
129	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	LDR3
130	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
131	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	
132	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (R3 ← MDR)	
133	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	

	Sel(3)	T(2)	Sel P1	P1 L	P1 O	SP L	SP O	X L	X O	Y L	Y O	RT L	RT O	R1 L	R1 O	R2 L	R2 O	R3 L	R3 O	R4 L	R4 O	IR load	IR out	MAR load	MAR out	MDR load	MDR out	MDR mem	RW	PC load	PC out	PC init	Sel ACC	ACC load	ALU code	F i	Programa de control	Instrucció
																Bits d'adreça de salt																								
134	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	LDR4
135	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
136	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	
137	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (R4 ← MDR)	
138	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
139	000	10	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (X ← IR) amb mask de 8 bits	X = num
140	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
141	000	10	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (Y ← IR) amb mask de 8 bits	Y = num
142	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
143	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	1	00011	0	EXE (ACC ← IR) amb mask de 8 bits	LDR1 Y + num
144	000	10	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	00010	0	EXE (MAR ← Y + ACC)	
145	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	
146	000	10	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (R1 ← MDR)	
147	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
148	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	1	00011	0	EXE (ACC ← IR) amb mask de 8 bits	LDR2 Y + num
149	000	10	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	00010	0	EXE (MAR ← Y + ACC)	
150	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	
151	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (R2 ← MDR)	
152	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
153	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	1	00011	0	EXE (ACC ← IR) amb mask de 8 bits	STRT Y + num
154	000	10	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	00010	0	EXE (MAR ← Y + ACC)	
155	000	10	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	00000	0	EXE (MDR ← RT)	
156	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	00000	0	EXE (escriptura en RAM)	
157	000	01	0	00	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
158	000	10	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00100	0	EXE (Y← Y + 1)	INC Y
159	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
160	000	10	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	00000	0	EXE (PC ← SP)	RET
161	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
162	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	10010	0	EXE (PC ←100)	INTERRUPTION
163	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	00000	0	EXE (MAR ← PC)	
164	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
165	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	1	0	0	0	00100	0	EXE (MDR MEM(MAR)) EXE PC ++	
166	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (IR ← MDR)	
167	000	11	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	BRA	

	Sel(3)	T(2)	Sel P1	P1 L	P1 O	SP L	SP O	X L	X O	Y L	Y O	RT L	RT O	R1 L	R1 O	R2 L	R2 O	R3 L	R3 O	R4 L	R4 O	IR load	IR out	MAR load	MAR out	MDR load	MDR out	MDR mem	RW	PC load	PC out	PC init	Sel ACC	ACC load	ALU code	F i	Programa de control	Instrucció
																Bits d'adreça de salt																								
168	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	00011	0	EXE (MAR ← IR) amb mask de 8 bits	
169	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (NOOP)	
170	000	10	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0	00000	0	EXE (MDR MEM(MAR))	LDRPORT1
171	000	10	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	00000	0	EXE (RPORT1 ← MDR)	
172	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
173	101	00	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	IF NOT RW GOTO 176	
174	000	10	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	Llegir Extern	
175	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	RWPORT1
176	000	10	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	Escriure Extern	
177	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	
178	000	10	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	EXE (RPORT1 ← RT)	MOV RPORT1, RTEMP
179	000	01	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	00000	0	GOTO 2	