

## Post-radiation-induced soft breakdown conduction properties as a function of temperature

Andrea Cester, Alessandro Paccagnella, Jordi Suñé, and Enrique Miranda

Citation: Applied Physics Letters 79, 1336 (2001); doi: 10.1063/1.1398329

View online: http://dx.doi.org/10.1063/1.1398329

View Table of Contents: http://scitation.aip.org/content/aip/journal/apl/79/9?ver=pdfcov

Published by the AIP Publishing



APPLIED PHYSICS LETTERS VOLUME 79, NUMBER 9 27 AUGUST 2001

## Post-radiation-induced soft breakdown conduction properties as a function of temperature

Andrea Cesteral and Alessandro Paccagnella

Dipartimento di Elettronica e Informatica, Universitá di Padova, Via Gradenigo 6A, 35131 Padova, Italy, and INFM, Unità di Padova-E4

Jordi Suñé

Departament d'Enginyeria Electrònica, Universitat Autònoma de Barcelona, 08193 Bellaterra, Spain

## Enrique Miranda

Departamento de Física-Facultad de Ingeniería, Universidad de Buenos Aires, 1063-Buenos Aires Argentina

(Received 11 May 2001; accepted for publication 9 July 2001)

When a thin oxide is subjected to heavy ion irradiation, a large leakage current similar to the soft breakdown can be produced. In this work, we have studied the radiation soft breakdown (RSB) after 257 MeV Ag and I irradiation by using a quantum point contact (QPC) model, which also applies to hard and soft breakdown produced by electrical stresses. We have also studied the temperature dependence of RSB current from 98 K up to room temperature, and found that the gate current after irradiation is strongly reduced by decreasing temperature. It is shown that this behavior can be attributed to a temperature dependence of the carriers supplied from the cathode rather than to a temperature-induced modification of the size and/or shape of the oxide RSB paths. © 2001 American Institute of Physics. [DOI: 10.1063/1.1398329]

submicron complementary metal-oxidesemiconductor (CMOS) devices can be successfully used in radiation-harsh environments by adopting some simple layout rules [such as guard rings and metal-oxidesemiconductor field effect transistor (MOSFET) with enclosed structures].1 At present, high-speed CMOS integrated circuits are fabricated in 0.18 µm technologies, with gate oxide thickness of 3 nm. In these thin oxides, the total trapped charge after exposure to ionizing radiation is quite modest, due to fast recombination or escape of an oxide trapped charge, leading to a negligible threshold voltage shift. Also, the MOSFET transconductance is only marginally affected by irradiation. On the contrary, the main reliability problem of thin oxides is the increase of the gate leakage current after irradiation (Radiation Induced Leakage Current), 2,3 which hampers the device lifetime. Recently, it has been shown that radiation induced soft breakdown (SB) can be produced in 4 nm oxides after exposure to irradiation with high linear energy transfer (LET) ions. 4 Such leakage current appears similar to the SB produced by electrical stresses.

Recently, both electrically induced oxide hard breakdown and SB were successfully modeled using a quantum point contact (QPC) picture.<sup>5,6</sup> The basic idea of the QPC model is that the conduction is strongly localized and flows through one or more breakdown paths. In the case of radiation, it is likely that we are dealing with several conductive paths generated by the ions passing across the oxide.<sup>4</sup> Each of these conductive paths should behave similarly to a single path generated after an electrical stress, but with a smaller area. Based on the QPC model, the lateral dimension of the breakdown paths is so small that the momentum in the direction perpendicular to propagation is quantized. If the path is narrow enough (as in the case of electrical SB), the ground subband  $E_0$  is above the Fermi level at the cathode, and the conduction can only take place by tunneling across an effective one-dimensional (1D) potential barrier. This barrier is not material related, but its height cannot be larger than that of the  $\rm Si/SiO_2$  system because electrons would be otherwise no longer laterally confined. This model leads to the exponential relation for the gate current:

$$I_g = A \times \exp[B(V_g - V_0)]. \tag{1}$$

The parameter A and B have been related to the height and thickness of the local barrier in the breakdown path. The offset voltage  $V_0$  is due to some potential drop in the substrate. Our present goal is to extend the validity of such a model also to the case of the radiation soft breakdown (RSB), and to investigate which is the effect of temperature on the spot parameters.

We have used square MOS capacitors on p-Si substrate with gate area  $A = 10^{-2}$  cm<sup>2</sup>. The oxide thickness was  $t_{\rm ox}$  = 4 nm. All capacitors were surrounded by a  $n^+$  ring in order to supply electrons when a positive gate voltage was applied. The gate is a N<sup>+</sup> polysilicon layer and the gate oxide has been thermally grown in a steam ambient and subjected to a high temperature N<sub>2</sub> anneal.

Heavy ions were supplied by the Tandem Van der Graaff accelerator of the Laboratori Nazionali di Legnaro, Instituto Nazionale de Fisica Nucleare (INFN), and Università di Padova, Italy. The ion species were 257 MeV $^{121}$ I and 257 MeV $^{107}$ Ag. with LET of 52.2 and 61.6 MeV cm $^2$ /mg, respectively. The gate current–voltage ( $I_g$ – $V_g$ ) characteristics were measured before and after irradiation at temperatures ranging from 98 to 300 K.

a)Electronic mail: cester@dei.unipd.it

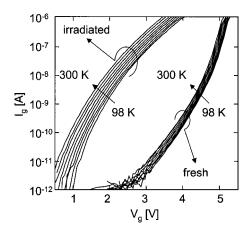


FIG. 1.  $I_g - V_g$  curves before and after irradiation measured at temperatures ranging between 98 and 300 K in a MOS capacitor with  $t_{ox}$ =4 nm are shown.

Figure 1 shows  $I_g - V_g$  characteristics of a MOS capacitor before and after irradiation with 32×10<sup>9</sup> Ag ions/cm<sup>2</sup>. A similar behavior has been observed for 12×10<sup>9</sup> I ions/cm<sup>2</sup> irradiation. The RSB appears as a large increase in the leakage current after irradiation with respect to the fresh device, as previously reported.<sup>4</sup> In addition, we show here that the RSB is largely affected by the measuring temperature. The RSB current significantly decreases when the temperature is reduced while the fresh  $I_g - V_g$  shows a weaker temperature dependence. Moreover, the main temperature effect on the RSB characteristics can be modeled as a rigid shift of the  $I_{\varphi}$  –  $V_{\varphi}$  curves parallel to the voltage axis. In Fig. 2, the RSB current measured in the same device at nine different temperatures has been plotted as a function of  $V_g - \Delta V(T)$  for both positive and negative polarity,  $\Delta V(T)$  being the voltage shift needed to overlap the curves measured at temperature T and at 300 K. All nine curves nicely overlap each other for the  $\Delta V(T)$  values shown in the inset of Fig. 2. Noticeably,  $\Delta V(T)$  varies almost linearly with T.

A comparison between experimental data and theoretical curves is shown in Fig. 3. An excellent fit is obtained when  $V_g > 2V$ , by using the A and B values shown in the inset. The value of  $V_0 = 0.6 \,\mathrm{V}$  at room temperature is the same as reported in Ref. 6 for electrical SB. A and B do not depend on

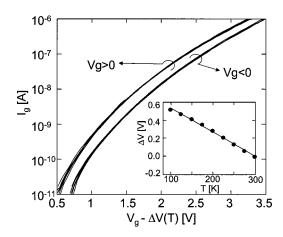


FIG. 2.  $I_g - V_g$  curves of Fig. 1 plotted as a function of  $V_g - \Delta V(T)$  are shown.  $\Delta V(T)$ , shown in the inset, is the temperature-dependent gate voltage shift required to overlap the curves measured at temperature T and at

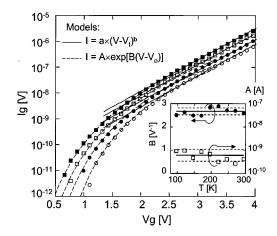


FIG. 3. Comparison between experimental results (symbols) and data derived from the QPC model [see Eq. (1) in the text] (solid lines) is shown. The different curves refer to the measurement temperatures T=98 ( $\bigcirc$ ) 173 ( $\bullet$ ), 248 ( $\square$ ), and 300 K ( $\blacksquare$ ). Curves derived form the empirical model  $(I=a\times (V-V_1)^b)$  are shown as dashed lines.

temperature, indicating that temperature has a negligible effect on the height, thickness, and shape of the 1D effective barrier associated to the weak spots generated by radiation. Since this is a barrier related to the quantum confinement, this also means that the geometry of the RSB paths does not depend on the temperature. This is consistent with the results of Fig. 2, since only  $V_0$  depends on temperature. In our opinion, the temperature-dependent shift,  $\Delta V(T)$ , only reflects the thermal occupancy of the electron states in the electrode regions adjacent to the RSB path. The state occupancy affects the electron supply to the RSB path and the voltage drops in the access region to the QPC.

In the low voltage region ( $V_g$ <2 V), the QPC model does not satisfactorily fit the experimental results. Nevertheless, they are nicely described by the empirical power law proper of SB conduction at low oxide fields:<sup>7</sup>

$$I_{\mathfrak{g}} = a \times (V_{\mathfrak{g}} - V_{\mathfrak{f}})^{b}. \tag{2}$$

 $V_t$  is temperature dependent, and we observed  $V_t(T) \approx \Delta V(T)$  (data are not shown for brevity). At room temperature, we found  $V_t \approx 0 \, V$  in agreement with previous results. We have verified even in this case that parameters a and b of Eq. (2) are not affected by temperature, as in case of A and B in Eq. (1), confirming that the RSB path properties do not change with temperature. According to the QPC model predictions, the parameters a and b must be correlated through:

$$b = k_1 \times \log(a) + k_2. \tag{3}$$

Even for RSB, this correlation is satisfied with  $k_1 = -0.81$ , and  $k_2 = -1.64$  as illustrated in Fig. 4 (filled symbols), together with the solid lines found for the SB, correlation. While the slope  $k_1$  has practically the same value for RSB and SB,  $k_2(RSB) > k_2(SB) = -3.27$ . In our opinion, this apparent disagreement is derived from the fact that many RSB spots are active after irradiation while only one SB spot appears after electrical stresses. Eqs. (1) or (2) should be modified accordingly as:

$$I_g = N \times A \times \exp[B \cdot (V_g - V_0)], \tag{4a}$$

his a age shift required to overlap the curves measured at temperature T and at subject to the terms at: http://scitation.aip.org/termsconditions. Downloaded to IP 300 K.  $I_g = N \times a \times (V_g - V_t)^p, \qquad (4b)$ 

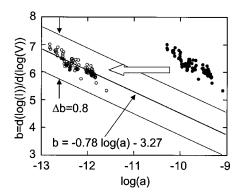


FIG. 4. Correlation between parameters a and b measured for RSB [see Eq. (3) in the text] (filled symbols) are shown. Results are compared to those previously reported for SB induced by electrical stress represented by the dotted line (average value) and solid line (data dispersion). Overlap is achieved by shifting the RSB data along the X axis by a constant (open symbols).

N being the number of radiation-induced spots. Hence, for N>1, the RSB experimental data should horizontally shift by  $\log(N)$  in Fig. 4 (open symbols), then nicely overlapping the previous SB results. In this way, we may evaluate the spot number N as 446 for  $32\times10^9$  Ag ions/cm<sup>2</sup> and N=255 spots for  $12\times10^9$  I ions/cm<sup>2</sup>, respectively, corresponding to a generation efficiency around  $1.7\times10^{-6}$  spot/ion. From the values of A, B, and N, we evaluated the average barrier height  $\phi=3.20$  and 2.82 eV, and the average spot area  $10^{-18}$  and  $10^{-17}$  cm<sup>2</sup>, for the Ag and I irradiation, respectively. The barrier thickness has been estimated around 2.5 nm independently on the ion source.

In summary, we have analyzed the radiation induced SB measured at different temperatures. We have applied the QPC model to explain the RSB conduction mechanism. Following this model, we have estimated the number of

radiation-generated spots, which are some hundreds after the total irradiation doses used in our experiments. This corresponds to a generation efficiency around  $1.7 \times 10^{-6}$  spot/ion. Moreover, by measuring the gate current at different temperatures, we have found that the parameters of the spots, i.e., the barrier height and thickness, do not depend on temperature. The large decrease of the RSB leakage current with decreasing temperature is only related to some change in the electron distribution in the regions of the electrodes adjacent to the RSB path. The presented results confirm that the nature of the RSB paths is essentially the same as that of the electrically induced SB paths.

The authors would like to acknowledge Dr. G. Ghidini (ST Microelectronics, Agrate Brianza, Italy) for the device supplied and for her scientific support. This work has been partially supported by MURST-Italy and CNR-Prog. Fin. MADESS-2. One of the authors (J.S.) acknowledges support from the Spanish DGES project BFM2000-0353.

<sup>&</sup>lt;sup>1</sup>A. Giraldo, A. Paccagnella, C. Dachs, F. Faccio, E. Heijne, P. Jarron, K. Kloukinas, and A. Marchioro, *Proceedings of the Third Workshop on Electronics for LHC Experiments*, London, UK, CERN Technical note CERN/LHCC97-60, pp. 139–143.

<sup>&</sup>lt;sup>2</sup> A. Scarpa, A. Paccagnella, F. Montera, G. Ghibaudo, G. Pananakakis, G. Ghidini, and P. G. Fuochi, IEEE Trans. Nucl. Sci. 44, 1818 (1997).

<sup>&</sup>lt;sup>3</sup> M. Ceschia, A. Paccagnella, A. Cester, A. Scarpa, and G. Ghidini, IEEE Trans. Nucl. Sci. 45, 2375 (1998).

<sup>&</sup>lt;sup>4</sup>M. Ceschia, A. Paccagnella, S. Sandrin, G. Ghidini, J. Wyss, M. Lavalle, and O. Flament, IEEE Trans. Nucl. Sci. 47, 566 (2000).

<sup>&</sup>lt;sup>5</sup> J. Suñé, E. Miranda, M. Nafría, and X. Aymerich, Appl. Phys. Lett. 75, 959 (1999).

<sup>&</sup>lt;sup>6</sup>E. Miranda and J. Suñé, Appl. Phys. Lett. **78**, 225 (2001).

<sup>&</sup>lt;sup>7</sup>E. Miranda, J. Suñe, R. Rodríguez, M. Nafría, and X. Aymerich, IEEE Electron Device Lett. **20**, 265 (1999).

<sup>&</sup>lt;sup>8</sup>J. Suñe and E. Miranda, Proc. IEEE International Electron Devices Meeting, 533 (2000).