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A solar transistor and photoferroelectric memory

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Transistors switch a semiconductor *conductive* state (or *on*-state) and an *insulator* state (or *off*-state) by means of a third additional terminal known as gate [1]-[3]. An external bias between their two flow electrodes is always required for channel carriers to move, and this bias represents an undesirable yet unavoidable energy consumption. Harvesting the photon energy, a photovoltaic junction would already act as a self-powered current source, but incipient photovoltaic switches do not have a stable low energy consumption state [4]-[10]. Here, we report a new photo-ferroelectric transistor device concept based on a ferroelectric oxide (FE-oxide)/organic semiconductor heterojunction, where an insulating FE-oxide (Pb(Zr_xTi_{1-x})O₃) acts as a reversibly polarizable electron transport media of the organic bulk heterojunction (P3HT:PCBM) photogenerated carriers. Therefore we demonstrate two terminal (i.e. *gate-less*), low-cost (metal-oxide solution-processed), self-powered and non-volatile phototransistors that switch between bi-stable *on* and *off* states and with a low energy consumption in the *off* state.

A phototransistor, photo-switch or photo-FET [11]-[13] can be defined as a three-terminal device whose output can be simultaneously and independently controlled by light or voltage [14]. Ideally, the phototransistor concept should be integrated into a vertical (sandwich-like) two-terminal device for larger density of miniaturization [15] while keeping a normally-off state for better dissipation efficiency and easier control [16]. A reliable, cleanroom-less, low cost two-terminal phototransistor would represent a paradigm

shift for the next generation of sustainable embodiments such as photodetectors, smart windows, flexible detector systems and optical memories [17]. Nevertheless, such a device is unconventional and challenging since a photovoltaic semiconductor Schottky or *p-n* junction does not switch as internal fields and chemical potentials are not bi-stable [16]. There are however several photo-switchable mechanisms that can result in diverse types of photovoltaic responses; these include ferroelectric photovoltaics [4]-[6], light-induced ionic drift in halide perovskites [7], [8], photovoltaic resistive switching [9] and the photochromic effect [10].

Among the photovoltaic mechanisms enabling switching responses, ferroelectric oxides (FE-oxide) possess useful features in their switchable properties (a changeable direction of current and voltage by polarization switching) and in their anomalous photovoltaic effect that can generate photovoltages much bigger than the band-gap [18] [19]. Nevertheless, their inherently low photocurrents (in the range of nA-μA/cm²) have resulted in poor overall photovoltaic efficiency [19]-[20]. A way to improve the photovoltaic current of FE-oxide solar cells is through the combination with organic [31]-[34] or inorganic light absorbers [21]-[27].

It has been theoretically [28] and experimentally demonstrated [29], [30] that ferroelectrics bend their electronic band structure and offsets with respect to adjacent metals and metal-oxides when switching the ferroelectric polarization. As illustrated in Fig. 1(a), it is the objective of this study to implement this unique feature to design a new class and more compact of two terminal photovoltaic transistors. Here, we demonstrate an efficient (normally-off) gate-less (two-electrode) phototransistor where the conductivity modulation is achieved by the polarization state of a FE-oxide interlayer while the efficient light harvesting channel region is a bulk organic heterojunction. The normally-off operation is a key and unreported device enabling feature so far, and the canonical bulk photovoltaic effect currents switch between positive and negative values and no in between on and off conductivity states.

In the proposed configuration, the wide bandgap FE-oxide layer acts as both the transparent electron transport material and as a switchable interface, as shown in Fig. 1 (b). Efficient light harvesters based on organic semiconductors (OS) are defined on top the FE-oxide. OS are cheap, flexible, light-weight, made with abundant materials and have low environmental impact [35]. Usefully, they are synthesized at lower

temperatures than oxides, so they can be grown on top of the ferroelectric layer without causing damage. As an additional advantage, the organic semiconductor absorbs light in the solar visible range, thus circumventing one of the key difficulties of working with photoferroelectrics, which is their normally wide bandgap that confines their optimum performance to the UV spectrum.

An organic bulk heterojunction is made of an electron *donor* (e.g. P3HT polymer) and an electron *acceptor* (e.g. PCBM fullerene) organic compounds; the *p*-type "donor" *absorbs* a photon while the *n*-type "acceptor" *acquires* an electron (Fig. 1 (b)). An efficient heterojunction network allows the donor polymer to contact a hole transport medium (HTM) and the acceptor fullerene to contact an electron transport medium (ETM), facilitating the extraction of charge without recombination.

The transport medium may be an oxide thin-film that (i) tunes the Schottky barrier alignment and, (ii) selects the majority carriers to avoid recombination [35]. The HTM must establish an anode ohmic contact and select holes. For this purpose, we select a thin layer of V_2O_5 [36]. Meanwhile, for the ETM we chose thin-films of the archetypical FE-oxide Pb(Zr_xTi_{1-x})O₃ (x=0.53) layer, hereafter labelled as PZT, coated on the glass/FTO substrate (see methods). Although ferroelectric PZT is often treated as an insulator, it is in fact an n-type widebandgap semiconductor [], so it is apt as an ETM. In addition, having switchable polarization means that its interfacial band bending and thus its contact resistivity can be tuned. This adds a new degree of freedom to the functionality of the device; specifically, the non-volatile nature of the polarization means that the photo-gated transistor can also act as a memory device.

The energy band structure and band alignment of the different layers of the (unpoled) ferroelectric photovoltaic transistor is shown in Fig. 1(c), as determined by ultraviolet photoelectron spectroscopy (UPS) and ultraviolet-visible spectrophotometry (UV-Vis) (see methods and SI). Because of the wide bandgap of the transparent conductive oxide (FTO) and the FE-oxide ETM (PZT), the great majority of the photons are able to reach the organic bulk heterojunction light absorbing layer. The photo-generated electrons and holes are then selectively collected by the ETM and HTM, (PZT and V₂O₅ respectively), which also act as barriers for minority carriers. The photo-generated electrons and holes are thus extracted from the core device at their respective metallic electrodes.

The photovoltaic performances (in particular, the photovoltaic current density) of the proposed device have been optimized by varying the PZT layer thickness and its anneal temperature, as shown in Fig. (2). The ferroelectric PZT perovskite phase was evidenced by an XRD scan for annealing temperatures higher than 600 °C with a preferential orientation in <110> (see SI). The FE-oxide layer exhibited excellent ferroelectric characteristics as shown by the typical ferroelectric hysteresis polarization-voltage curve as shown in Fig. 2(a). The remnant polarization at the FE-oxide surfaces is larger than 20 \Box C/cm². The FE-oxide conductivity is extremely small. At room temperature, PZT is a good dielectric insulator with a conductivity of less than 10⁻¹² \Box -1-cm⁻¹. The bandgap of the PZT lies in the ultraviolet (~3.6 eV), making the PZT layer poorly absorbing in the visible part of the spectrum (see SI).

The ferroelectric photovoltaic properties of the FTO/PZT/Ag structure were tested under AM1.5G irradiation conditions (see SI). The area of the devices was 0.18-0.20 cm², larger than usual for vertical photoferroelectric capacitors, which are typically micro-scale [37],[38]. In optimal conditions, only a very weak photovoltaic current of $j_{sc} = (3\pm2)\times10^{-3} \,\mu\text{A/cm}^2$ was observed for simple photo-ferroelectric capacitors, in agreement with previous state-of-the-art large-area ferroelectric devices [39]. In contrast, when OS and HTM layers (i.e., P3HT:PCBM and V₂O₅) are added on top of the FTO/PZT, the photovoltaic current increases up to $j_{sc} = 7.07 \pm 0.31 \, \text{mA/cm}^2$. This is an increase of more than three orders of magnitude in comparison to the bare FE-oxide structure. The enormous increase is due to the large visible-range absorption of the organic layer, which brings additional carriers to the interface with the ferroelectric.

The ferroelectric polarization introduces non-volatile transistor-effect functionality into the photovoltaic junction as shown in Fig. 3. In the following, the poling electrode is the top silver layer, while the bottom FTO is always grounded. Switching the polarization up or down changes the ETM conduction band offset and thus modulates the FE-oxide/OS contact resistance from a state of high resistance (or off) to a state of low resistance (or on). A FE-oxide downward poling (positive bias) results in a shift of the FE-oxide valence band towards deeper energies. Accordingly, the conduction band offset ($\Box E_c$) between the fullerene PCBM lowest unoccupied molecular orbital (LUMO n-type) and the PZT conduction band decreases. The lower energy

barrier results in a higher electron current. Conversely, a FE-oxide *upward* poling (*negative* bias) reduces the ionization potential of valence band electrons and $\Box E_c$ increases, and the high energy barrier results in reduced photocurrent. Meanwhile, neither *downward* polarization nor *upward* polarization [50] permits hole transport because PZT is n-type. The high resistivity of the ETM barrier layer blocks residual leakage current and sustain the *off*-state.

The performance of one of our on large area (0.2 cm^2) photovoltaic transistor devices is illustrated in Fig. 4. The standard poling process for this experiment is a DC bias applied to the silver terminal. The duration of the DC pulse was few seconds. A $\pm 20 \text{ V}$ DC bias pulse is enough to switch *on/off* the photovoltaic transistor for the thicker PZT, while the thinnest could be switched with $\pm 5 \text{ V}$. A *positive* poling results in negative $\Box E_c$ and no FE-oxide/OS interface barrier, enabling photocurrents of 5-10 mA/cm². In contrast, applying a negative voltage switches the polarization upward, thus raising the blocking barrier. This results in a drop of the photocurrent, which is only 0.01mA/cm^2 , a three order of magnitude difference with respect to the on-state. This *off* state can be reversed back applying positive bias to the anode again (DC bias $\pm 20 \text{ V}/10 \text{ s}$). The PV-Switch behavior is consistent after subsequent poling cycles (see Fig. 4(c)). It must be also mentioned that, consistent with the band alignment explanation, the conductivity differences upon the polarization state of the FE-oxide layer are preserved in the dark, resulting in a resistive switching device [15].

The main lesson from this investigation is that ferroelectrics can be exploited not just as photovoltaic materials in their own right, but also as (switchable) charge transport layers photoelectronic devices. In this context, ferroelectric oxides add new functionalities to the structure; in the present case, a two- electrode self-powered photo-transistor device concept has been demonstrated.

The new photo-ferroelectric transistor concept is a sandwiched organic light-absorbing layer in between an electron transport layer (*n*-type FE-oxide) and a hole transport layer (*p*-type metal oxide). Under sunlight illumination, the absorbing organic bulk heterojunction acts a self-powered source of photo-generated electron and holes that are driven towards their respective electrodes. Conventional photodiodes do not switch, but here, the modification of the electron transport layer barrier offsets due to the ferroelectric polarization enables

the *on-off* switching of the photodiode. Additionally, the conventional phototransistor's area usage and interconnection complexity is greatly enhanced by moving from the standard in-plane three-electrode architecture to a vertical, two-electrode photodiode-like architecture.

While the new device concept may be described as a photo-transistor, the present design is multifunctional and may be used for other photonic functionalities. For example, the present device is also photonic ferroelectric random-access memory (FRAM) where the *bits* are written via an electrical pulse, but read using a light pulse that excite a high current in the *on* state, but low current in the *off* state.

Methods

Materials

The synthesis of the PZT was done as follows: Zr-isopropoxide (Zr[OCH(CH₃)₂]₄ -70 wt. % in 1-propanol-Sigma) and Ti-isopropoxide (Ti[OCH-(CH₃)₂]₄ (97%) Sigma) were mixed and then diluted in acetic acid and n-propanol. Afterwards, Pb acetate (10% excess) [lead(II) acetate trihydrate, Pb(CH₃CO₂)₂•3H₂O (99.999%)] was dissolved into the above solution heating the solution in a silicon oil bath to 85 °C for dissolving the Pb precursor completely. After the cooling down of the solution, acetic acid and distilled water were added to modify the Pb-Zr-Ti solution and adjust the solution to make 40 ml 0.4 M. The active polymer:fullerene layer was formed mixing 98% rr-poly(3-hexylthiophene-2,5-diyl) (P3HT) and PCBM ([6,6]-Phenyl-C61-butyric acid methyl ester,99.5%). Synthesis of the V₂O₅·H₂O xerogel was carried out by the cationic exchange method. Briefly, 4.5 g of sodium metavanadate (NaVO₃; Sigma) were dissolved in water at 80 °C. Once cold, the solution was passed through a cationic exchange DOWEX50 WX2 50-100 (Sigma). The initial colourless solution changed to yellow-orange corresponding to the formation of the metavanadic acid. The polycondensation of the vanadic acid results in the formation of the V₂O₅ gel (red) after one month standing at room temperature. The final sol-gel has a concentration of 1 9-10 mg/ml and is stable at RT for several months.

Device Fabrication

The FTO substrates were bought from Solems, model ASAHI 100 by CVD of 1.1 mm thickness (70-100 ohm resistance, thickness of FTO layer 800 Å.) and were etched with a mixture of Zn powder (≥99%, Aldrich), and hydrochloric acid (HCl) (ACS reagent 37% from Sigma Aldrich). The etched substrates were then cleaned with soap water, mili-Q water and ethanol (99.5%), dry with N₂ and treated under UV-ozone treatment for 20 min before use.

Once cleaned, a 80 \Box 1 of PZT solution was spin-coated on top of the FTO. The solution was dried in the hot plate for 5 min at 150°C. The PZT layer was further annealed (400-650 °C) for 10 min in air. Depending on the final desired thickness this coating procedure can be repeated up to three times or the PZT solution diluted in acetic acid. The P3HT:PCBM solution (30 mg/ml in Chlorobenzene) was spin-coated on top of the oxide

layer at 1000 rpm, and then, a solution of V_2O_5 sol-gel:Isopropanol (1:1) was spin-coated at 1000 rpm and annealed at 120°C for 5-10 min in a hot plate under ambient conditions. Finally a layer of 100 nm of the Ag metal was evaporated on top of the V_2O_5 layer in a BOC EDWARDS AUTO 306 thermal evaporator using a shadow masks. After device fabrication, thermal annealing was carried out at 120 °C for 10 minutes in air.

Material and Device Characterization

X-ray powder diffraction (XRD) analyses between 5 and 120 degrees in a RIGAKU Rotaflex RU200 B instrument, using CuKα1 radiation.

The *X-ray photoelectron spectroscopy (XPS)* and *ultraviolet photoelectron spectroscopy (UPS)* measurements were performed with a Phoibos 150 analyzer (SPECS GmbH, Berlin, Germany)) in ultra-high vacuum conditions (base pressure 3E-10mbar). XPS measurements were performed with a monochromatic Kalpha x-ray source (1486.74eV) and UPS measurements were realized with monochromatic HeI UV source (21,2eV). Work function determination was done by applying bias of -10V at the sample.

The solar simulation was carried out with a Steuernagel Solarkonstant KHS1200. Light Intensity was adjusted at 1000 W/m with a bolometric Zipp&Konen CM-4 pyranometer. Calibration of the sun simulator was made by several means, with a calibrated S1227-1010BQ photodiode from Hamamatsu and a minispectrophotometer from Ava-Spec 4200. The AM1.5G reference spectrum was according to an ASTMG173 standard. Solar decay and IV-curves were measured using Keithley 2601 multimeter connected to a computer and software.

The UV-visible analysis of solutions and thin films were made in a Shimadzu 1800 spectrophotometer.

The IPCE analyses were carried out with a QE/IPCE measurement System from Oriel at 10 nm intervals between 300 and 700 nm. The calibration was carried out using a Hamamatsu S1227-101030 diode. The results were not corrected for intensity losses due to light absorption and reflection by the glass support.

The ferroelectric characterization was performed with a precision LC tester LCII Ferroelectric Test System unit of Radiant technologies attached to a probe station.

The TEM lamella was defined by a focused ion beam. The FIB has circuit editing capability and nanomachining capabilities. The FIB used in this work is a Zeiss 1560XB Cross Beam able to work with sample of size up to a 150 mm. The field emission focused ion beam optics have a liquid Gallium ion emitter with acceleration voltage of 3 kV to 30 kV and beam current of 1 pA - 20 nA with a maximum resolution of 7.0 nm. The system contains a multiple injector needle system for 5 different gases including gas chemistry solution for ion or electron beam deposition of Platinum-containing material.

The TEM inspection has been carried out by a high-resolution FEI Tecnai F20 S/TEM equipped with an EDX.

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Author Contributions

APT, MLC and GC conceived the experiments. APT was in charge of the experimental and characterization work assisted by AL, QB and IS and. All authors analysed the data and co-wrote the paper.

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Figure Captions

Fig. 1. (a) Typical device configuration of a conventional (three-terminal) photo-transistor. Here we propose a new two-terminal phototransistor concept (photo-ferroelectric transistor). (b) Schematic view of the FE-oxide/organic semiconductor (OS) photovoltaic switch with a configuration TCO/FE-oxide/OS/HTM/Metal. The cathode TCO (transparent conductive oxide) is fluorine doped tin oxide (FTO). The ferroelectric layer is a compact PZT thin-film with formula Pb(Zr_xTi_{1-x})O₃ (x=0.53). The organic semiconductor (OS) absorber is a bulk heterojunction thin film made of the P3HT polymer, *donor*, and the PCBM fullerene, *acceptor* and the hole transport material (HTM) is a layer of V₂O₅. The metal anode is silver. (c) Experimental energy band alignment of the (unpoled) photovoltaic transistor structure as determined from UPS, XPS and UV-Vis. Electrons and holes are photo-generated in the OS absorbing layer and selectively collected by the ETM and HTM and transported to the outer FTO and Ag electrodes, respectively. Poling of the structure results in a modification of the band alignment of the PZT/PCBM interface and a transistor effect.

Fig. 2. (a) Representative hysteresis loop of the FTO/PZT/Ag capacitor. The optimization of the FE-oxide/P3HT:PCBM short-circuit current was carried out by means of the PZT annealing temperature (i.e. ferroelectric polarization) and the PZT concertation (i.e. PZT thickness). In this panel, (b) represents the FE-oxide/P3HT:PCBM short-circuit current as a function of the PZT anneal temperature. A total of 40 devices were fabricated for each temperature in 5 different batches. (c) Illustrates the evolution of j_{sc} as a function of the concentration of the 0.4M Pb-Zr-Ti solution in parts of acetic acid (c/c0). A total of 74 devices were fabricated in 7 different batches (4-5 for each c/c0).

Fig. 3. Poling the device as depicted in (a) and (b) leads to significant variations of the FE-oxide/PCBM LUMO band offset ($\Box E_c$). *Downward* polarization (c) facilitates current flow while *upward* polarization (d) enhances the FE-oxide/OS *n*-type LUMO band offset. The conduction band offset acts as an electron barrier blocking the photovoltaic current (*off*-state of the ferroelectric photovoltaic transistor).

Fig. 4. (a) Photovoltaic current AM1.5G (1000 W/m²) of the FE-oxide/P3HT:PCBM photovoltaic transistor showing the *on* and *off* states (both under illumination). (b) Dark current *on* and *off* state showing already *resistive switching* behavior. (c) The switchable behavior is consistent after subsequent poling cycles.

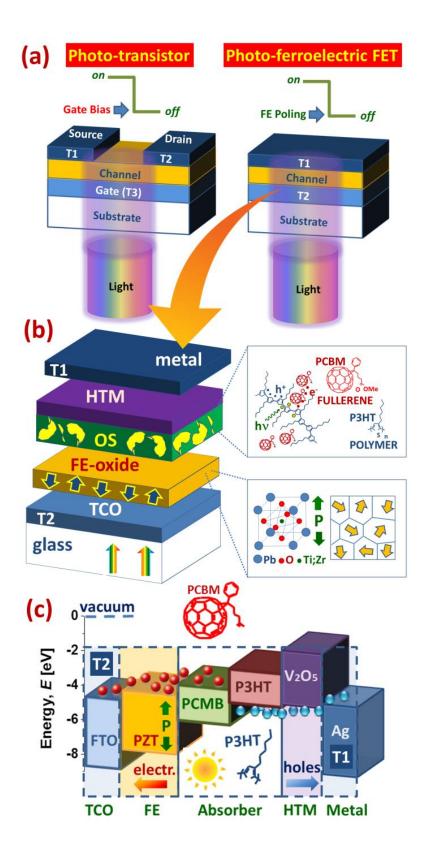


Fig. 1.

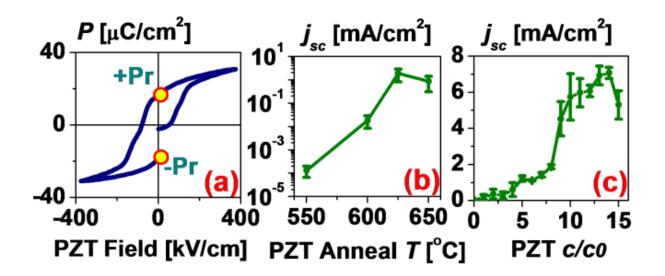


Fig. 2.

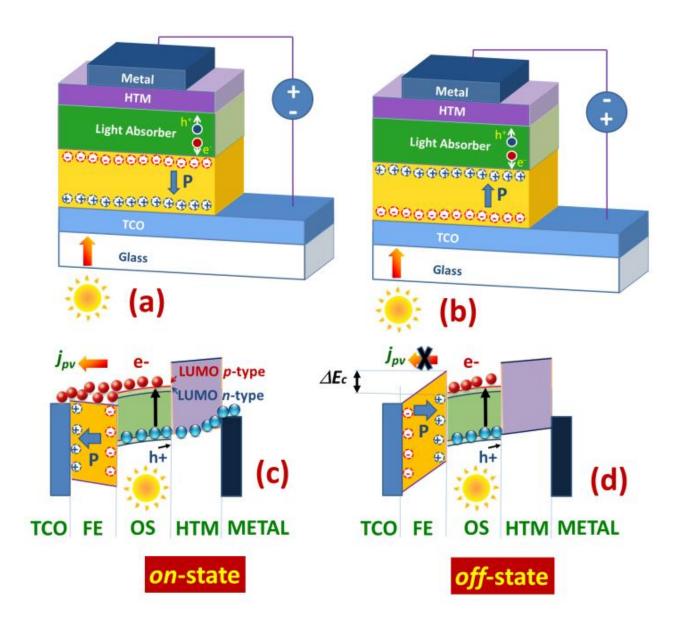


Fig. 3.

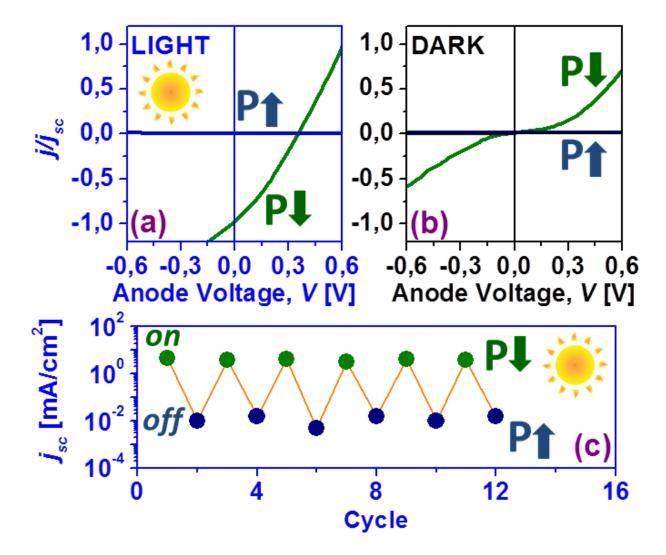


Fig. 4.