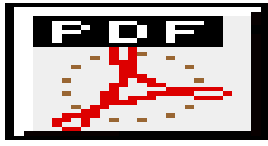
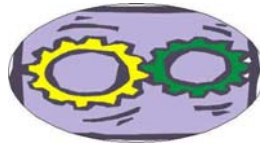


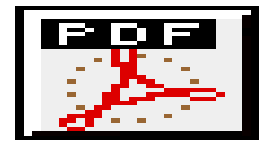
Desenvolupament d'interfícies HW per a FPGA des de Matlab



[MEMORIA](#)



[CODIS FONT](#)



[DATASHEETS](#)

Andreu Marzal i Marquet

1105632

ETSE-UAB Setembre 2007